

FB2012 Series

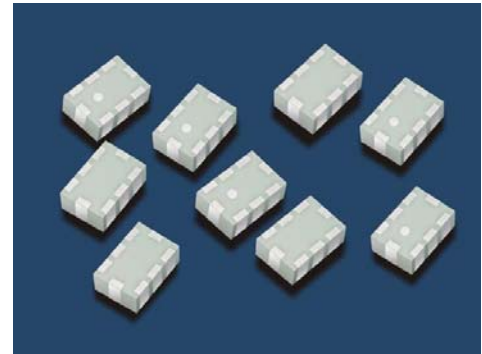
Multilayer Chip Band Pass Filter + Balun

Features

❖ Monolithic SMD with small, low-profile and light-weight type.

Applications

❖ 0.8 ~ 6 GHz wireless communication systems, including DECT/PACS/PHS/GSM/DCS phones, WLAN card, Bluetooth modules, etc.



Specifications

Part Number	Freq. Range (MHz)	Unbalanced Impedance (ohm)	Balanced Impedance (ohm)	Insertion Loss @ BW (dB)	VSWR @ BW	Phase Diff. (degree)	Amp. Diff. (dB)	Attenuation (dB)
FB2012-07N2R4C_	2400 ~ 2500	50	Conjugate match to BC series of CSR	3.5 max.	2.0 max.	180±10	1.5 max.	48 min. @ 880~960MHz 30 min. @ 1710~1880 MHz 30 min. @ 1880~1990 MHz 25 min. @ 4800~5000MHz 25 min. @ 7200~7500MHz

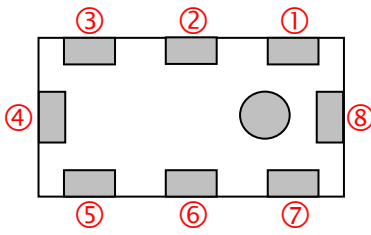
Q'ty/Reel (pcs) : 4,000
 Operating Temperature Range : -40 ~ +85 °C
 Storage Temperature Range : -40 ~ +85 °C
 Storage Period : 12 months max.
 Power Capacity : 1W max.

Part Number

FB **2012** - **07** **N** **2R4** **C** **□** **□**
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧

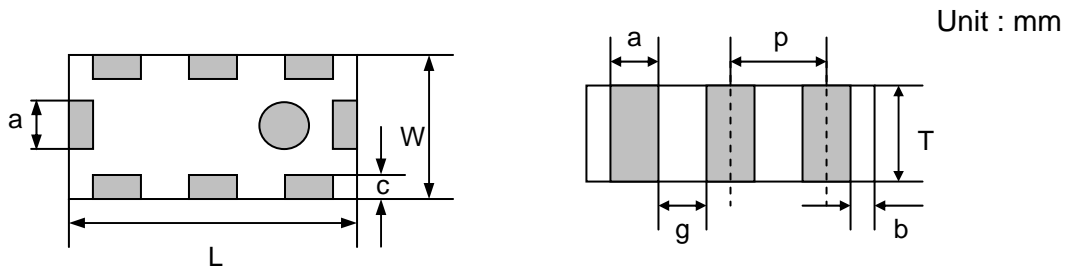
① Type	FB : Band Pass Filter + Balun	② Dimensions (L x W)	2.0 x 1.2 mm
③ Balanced Impedance	07 : Conjugate match to CSR chipset	④ Material Code	N
⑤ Central Frequency	2R4 : 2400MHz	⑥ Specification Code	C
⑦ Packaging	T: Tape & Reel B: Bulk	⑧ Soldering	=lead-containing /LF=lead-free

Terminal Configuration



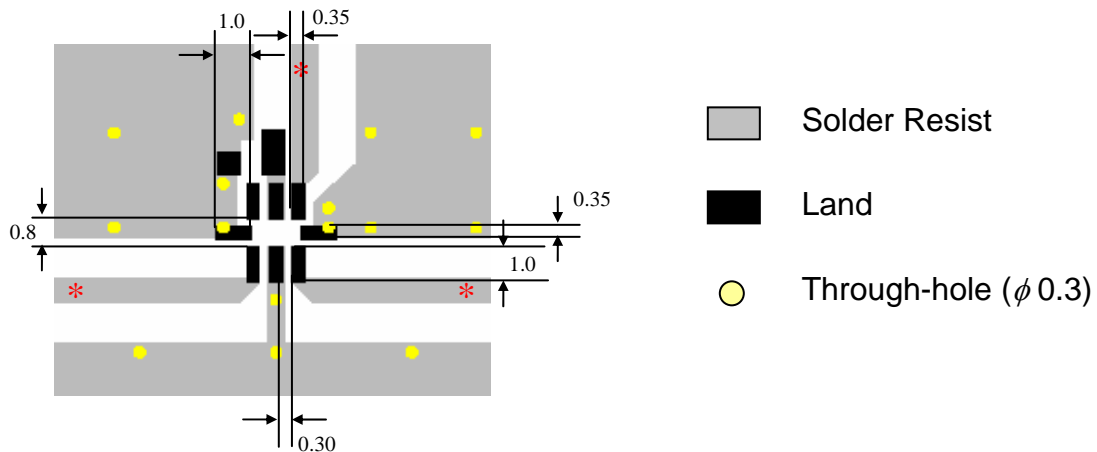
No.	Terminal Name	No.	Terminal Name
①	Unbalanced Port	⑤	Balanced Port
②	NC or DC Feed	⑥	GND
③	NC	⑦	Balanced Port
④	GND	⑧	GND

Dimensions



Mark	L	W	T	a	b	c	g	p
Dimensions	2.0 ± 0.2	1.25 ± 0.2	0.7 ± 0.1	0.3 ± 0.1	0.2 ± 0.1	0.3+0.1 /-0.2	0.35 ± 0.1	0.65 ± 0.05

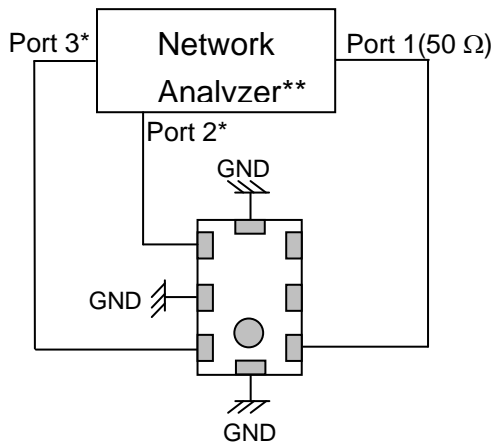
With DC feed



* Line width should be designed to match 50Ω characteristic impedance, depending on PCB material and thickness.

** By-pass capacitor should be connected when feeding DC power.

Measuring Diagram



Port 1: Unbalanced Port

Ports 2 and 3: Balanced Port

$$IL = S_{ds21}$$

$$RL = S_{ss11}$$

$$\text{Amp_balance} = \text{dB}(S(2,1)/S(3,1))$$

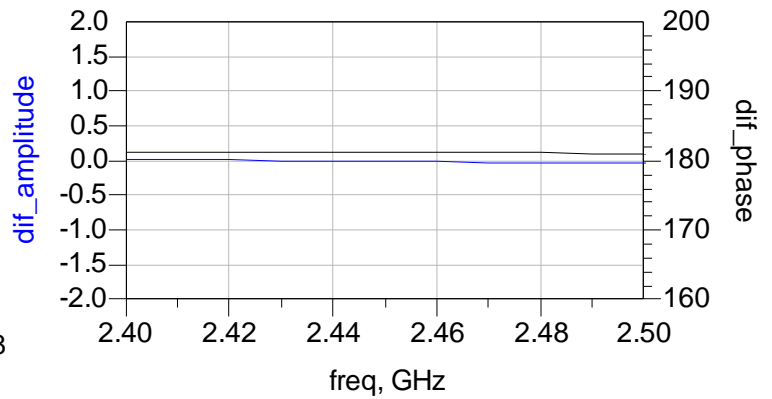
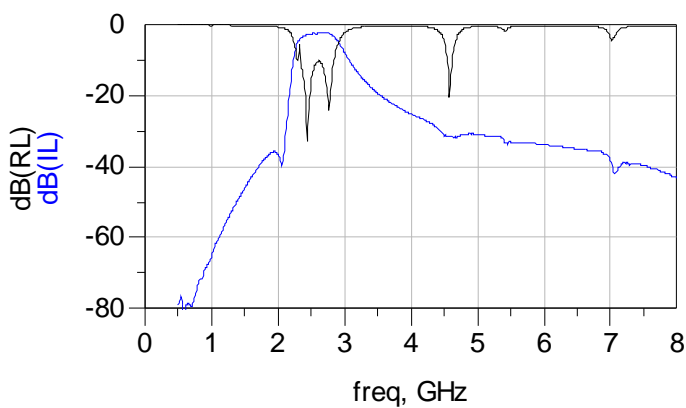
$$\text{Phase_balance} = \text{Phase}(S(2,1)/S(3,1))$$

*Impedance for ports 2 and 3

= Conjugate to Balanced Impedance/2

**E5071B from Agilent

Typical Electrical Characteristics (T=25°C)

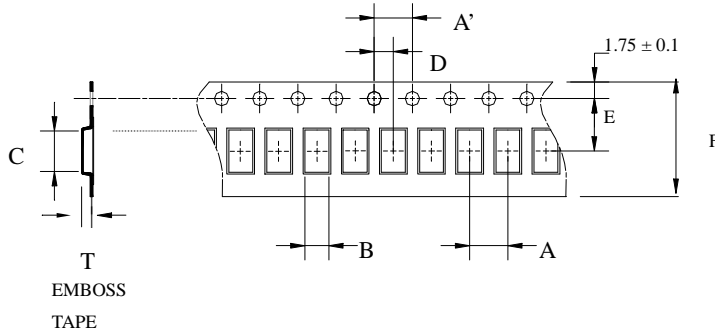


Notes

- ❖ The contents of this data sheet are subject to change without notice. Please confirm the specifications and delivery conditions when placing your order.

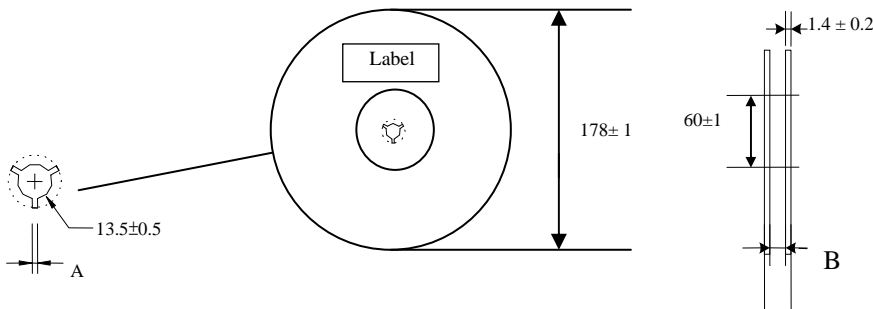
Taping Specifications

❖Tape Dimensions (Unit: mm) & Quantity



Type	A	A'	B	C	D	E	F	T	Quantity/reel	Tape material
2012	4.0±	4.0±	1.40±	2.15±	2.0±	3.5±	8.0±	1.00±	4,000pcs	Plastic (Embossed)
	0.1	0.1	0.05	0.05	0.05	0.1	0.1	0.05		

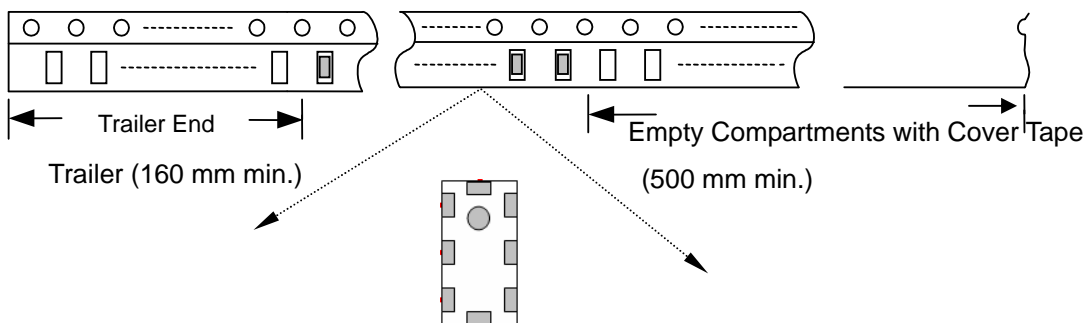
❖Reel Dimensions (Unit: mm)



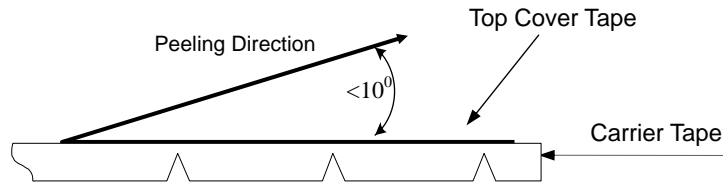
Label: Customer's Name,
ACX P/N, Q'ty, Date,
ACX Corp.

Type	A	B
2012	2.3±0.5	9.0±0.3

❖Leader and Trailer Tape



❖ **Peel-off Force**



Peel-off force should be in the range of 0.1 – 0.6 N at a peel-off speed of 300 ± 10 mm/min .

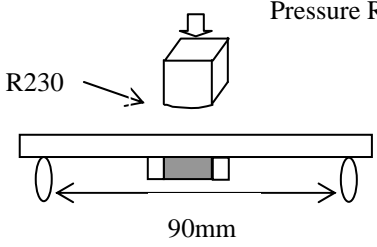
❖ **Storage Conditions**

- (1) Temperature: $+5 \sim 35^{\circ}\text{C}$, relative humidity (RH): 45~75%.
- (2) Non-corrosive environment.

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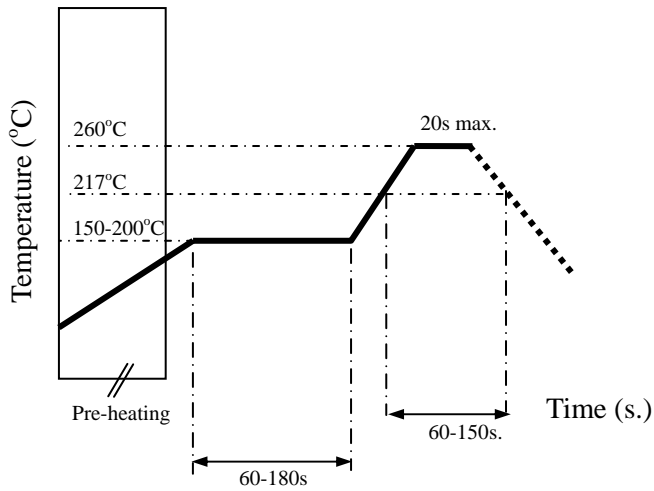
Mechanical & Environmental Characteristics

Item	Requirements	Procedure
Solderability	<ol style="list-style-type: none"> No apparent damage More than 95% of the terminal electrode shall be covered with new solder 	<ol style="list-style-type: none"> Preheat: $120 \pm 5^{\circ}\text{C}$ Solder: $245 \pm 5^{\circ}\text{C}$ for 5 ± 1 sec
Soldering strength (Termination Adhesion)	<ol style="list-style-type: none"> 1kg minimum 	<ol style="list-style-type: none"> Solder specimen onto test jig. Apply push force at 0.5mm/s until electrode pads are peeled off or ceramic are broken. Pushing force is applied to longitude direction
Deflection (Substrate Bending)	<ol style="list-style-type: none"> No apparent damage 	<ol style="list-style-type: none"> Solder specimen onto test jig (FR4, 0.8mm) using the recommend soldering profile. Apply a bending force of 2mm deflection 
Heat/Humidity Resistance	<ol style="list-style-type: none"> No apparent damage Fulfill the electrical specification after test 	<ol style="list-style-type: none"> Temperature: $85 \pm 2^{\circ}\text{C}$ Humidity: 90% ~ 95% RH Duration: 1000 ± 48hrs Recovery: 1-2hrs
Thermal shock (Temperature Cycle)	<ol style="list-style-type: none"> No apparent damage Fulfill the electrical specification after test 	<ol style="list-style-type: none"> One cycle/step 1 : $125 \pm 5^{\circ}\text{C}$ for 30 min step 2 : $-40 \pm 5^{\circ}\text{C}$ for 30 min No of cycles : 100 Recovery: 1-2 hrs
Low Temperature Resistance	<ol style="list-style-type: none"> No apparent damage Fulfill the electrical specification after test 	<ol style="list-style-type: none"> Temperature: $-40 \pm 5^{\circ}\text{C}$ Duration: 500 ± 24hrs Recovery: 1-2hrs

Soldering Conditions

❖ Typical Soldering Profile for Lead-free Process

Reflow Soldering :



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